Frequency-Domain Spectral Balance Using the Arithmetic Operator Method

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Abstract — A frequency-domain spectral balance method for the simulation of nonlinear analog circuits with multidimensional nonlinearities is presented. The method is coupled with a combination of Shamanskii and block Newton iteration schemes to produce an efficient general-purpose simulator. The technique is verified with measurements of a MESFET amplifier with single-tone and two-tone excitations.

I. Introduction

ICROWAVE circuit designers have traditionally relied on the use of experimental modifications to ensure that their designs meet required specifications. This practice, however, is becoming increasingly impractical as circuit dimensions shrink and circuit complexity increases. Thus there is an increasing interest in the computer-aided design of microwave circuits. A frequency-domain spectral balance method is developed here for the evaluation of the current/voltage relation of multidimensional nonlinearities (e.g., current through a nonlinearity being a function of two or more voltages in the circuit). This is coupled with a combination of Shamanskii and modified Newton iteration schemes to yield an efficient and accurate simulator for the analysis of nonlinear circuits having multifrequency excitations.

Although the methods for analyzing nonlinear circuits are varied, they are all based on solving a set of nonlinear differential equations resulting from application of Kirchhoff's voltage law and Kirchhoff's current law using the constitutive relations (i.e., the element characteristics). The methods fall into three groups according to the way in which the nonlinear elements are treated: time-domain methods, hybrid (harmonic balance) methods, and frequency-domain methods. The work we report here is of the frequency-domain type; that is, it avoids explicit time-domain calculations. This is accomplished through expansion of the input-output characteristics of the nonlinear elements in a set of basis functions of which there are three

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basic types: Volterra series [1]-[6], algebraic functional expansion [7]-[9], and power series. With frequency-domain spectral balance analysis, power-series-based techniques are proving to be the most general methods for the analysis of nonlinear circuits with multifrequency large-signal excitations. This approach has been investigated by a number of researchers as the basis for hand calculations as well as for computer-based simulations [10]-[17]. We continued the development of this type of series [18], resulting in the form (called a generalized power series)¹

$$Y_{\omega q} = \sum_{n=0}^{\infty} a_{\omega q, n} \left\{ \left[\sum_{k=0}^{K-1} x_k (t - \tau_k) \right]^n \right\}_{\omega q}$$
 (1)

where $Y_{\omega q}$ is the frequency-domain system output at radian frequency ω_a , n is the order of each power series term, $a_{\omega q, n}$ is an ω_q and n dependent complex coefficient, τ_k is a time delay that depends on the input frequency ω_k , x(t) is the independent variable, and the notation $\{f(x)\}_{\omega q}$ represents the phasor form of the ω_a component of the time-domain function f(x). A complex power series with frequency-dependent time delays can simply incorporate many memory effects, and only a few terms of the series expansion may be required. Therefore, a much larger class of systems can be mathematically modeled by a generalized power series (GPS) expansion than by a conventional power series expansion. For most microwave applications, circuits are typically made up of only a few circuit elements so that simulation can proceed without using complex coefficients. Time delays can be included by phase shifting of the input signal.

With the GPS description of the nonlinear system, algebraic formulas for the system output when the input is a sum of sinusoids have been developed [18]–[20]. This method uses an input/output transform table (the intermodulation product descriptions) and is more accurately referred to as generalized power series analysis using the table method (GPSA-TM) to differentiate it from the extension developed here. The formulas used in GPSA-TM, however, are complicated and difficult to understand. In addition, using the transform table in this method produces many repetitive calculations which degrade the circuit analysis efficiency.

¹The nomenclature has been changed slightly for compatibility with the work presented here.

GPSA-TM is practically restricted to one-dimensional (1-D) nonlinearities. It has been extended to two-dimensional (2-D) nonlinearities but the formulas are even more complicated [21] and its computer implementation is expected to be inefficient. In Section III, a newly developed frequency-domain spectral balance technique, the generalized power series analysis using the arithmetic operator method (GPSA-AOM) is presented. GPSA-AOM is straightforward, easy to implement, and handles multidimensional nonlinearities. It is also considerably faster than the GPSA-TM for single dimensional nonlinearities.

II. DEVELOPMENT OF CIRCUIT EQUATIONS

One approach used to solve the system of nonlinear equations is to formulate the nonlinear problem so that a Newton iteration scheme can be used. In this section we develop a block matrix formulation of the nonlinear circuit equations and combine this with a Newton iteration scheme which uses a combination of full Newton iteration, block Newton iteration, and chord iteration [22] to obtain a fast nonlinear analog circuit simulator. In the formulation of the nonlinear circuit equations, no explicit separation of a circuit into linear and nonlinear subcircuits is made.

A. Circuit Equations

If K different frequency components exist in an analog nonlinear circuit, computer-aided analysis in the frequency domain solves a system of nonlinear equations:

$$M(x)x = y \tag{2}$$

where

$$\mathbf{x} = \begin{bmatrix} \mathbf{x}_{\omega 0}^T & \mathbf{x}_{\omega 1}^T & \cdots & \mathbf{x}_{\omega (K-1)}^T \end{bmatrix}^T.$$
 (3)

Here T indicates the transpose and $x_{\omega k}$ is the variable vector to be solved which contains the necessary node voltages and edge currents at radian frequencies ω_k ; the source vector y, which includes all the independent sources, is similarly defined as x in (3); and the x-dependent matrix M(x), the modified nodal admittance matrix [23], is the *circuit matrix* of the analog nonlinear network.

Solution of the nonlinear circuit equations (2) is based on the process of successive approximations. After each iteration i, a new approximation of the variable vector, ${}^{i}x$, produces an *induced* source vector $y({}^{i}x)$:

$$M(^{i}x)^{i}x = y(^{i}x). \tag{4}$$

Newton's method, or a variant of it, forms the basis of most iterative procedures in nonlinear circuit analysis. This method is based on the general relationship

$$f(x) = 0$$

where $f(^ix)$ is a vector with the same dimension as the variable vector x. It is composed of the absolute values of the corresponding entries of the difference vector between the independent source vector y and the induced source vector $y(^ix)$. The iteration formula for Newton's

method is

$$i^{i+1}x = {}^{i}x - J^{-1}({}^{i}x) f({}^{i}x), \qquad i = 0, 1, 2, \cdots$$
 (5)

where $J^{(i}x)$ is the Jacobian matrix of f in the *i*th iteration and $J^{-1}(ix)$ is its inverse.

Fortunately, in solving the nonlinear circuit equations (2) using iterative procedures (5), the whole *circuit matrix* $M({}^{i}x)$ need not be calculated. Both the Jacobian matrix $J({}^{i}x)$ and the induced source vector $y({}^{i}x)$, which forms the error function $f({}^{i}x)$, can be obtained partially from the linear part of the circuit matrix M and partially from the variable vector ${}^{i}x$ using nonlinear circuit analysis techniques either in the frequency domain (power series analysis) or in the hybrid domain with Fourier transform techniques (harmonic balance method).

B. Circuit and Jacobian Matrices

The circuit matrix M(x) in (2) can be viewed as a matrix composed of K by K block matrices:

$$=\begin{bmatrix} M_{0,0}(x) & M_{0,1}(x) & \cdots & M_{0,K-1}(x) \\ M_{1,0}(x) & M_{1,1}(x) & \cdots & M_{1,K-1}(x) \\ \vdots & \vdots & \ddots & \vdots \\ M_{K-1,0}(x) & M_{K-1,1}(x) & \cdots & M_{K-1,K-1}(x) \end{bmatrix}.$$

Each block matrix $M_{q,k}(x)$ is a matrix with input frequency index k and output, or objective, frequency index q, and has the same $m \times m$ where m is the dimension of the corresponding vector $\mathbf{x}_{\omega q}$ in (3). Reducing $\mathbf{x}_{\omega q}$ to its minimum required size will always reduce the size of the matrix M(x) and decrease the complexity in circuit analysis. The minimum required variables of $\mathbf{x}_{\omega q}$ include all the node voltages of the admittance-type nonlinear elements, all the edge currents of the impedance-type nonlinear elements in the circuit, and any node voltages or edge currents of interest for the final results.

Entries in each matrix $M_{q,k}(x)$ may not be x-dependent because they are generated from all the elements of the circuit. In most cases, linear elements of the circuit create entries in $M_{q,k}(x)$ which are independent of the variable x, and nonlinear elements add x-dependent values to some of the entries. However, by using the modified nodal admittance matrix, some nonlinear elements, such as nonlinear inductors, will create x-independent entries as well. If $M_{ID(q,k)}$ represents the modified nodal admittance matrix generated from all the linear elements of the circuit plus those x-independent entries generated from all the nonlinear elements of the circuit, we have

$$\boldsymbol{M}_{q,k}(\boldsymbol{x}) = \boldsymbol{M}_{ID(q,k)} + \boldsymbol{M}_{D(q,k)}(\boldsymbol{x})$$

where the x-dependent matrix $M_{D(q,k)}(x)$ derives from the nonlinear elements, and matrix $M_{ID(q,k)}$ is always a zero matrix if q is not equal to k.

Similarly, the Jacobian matrix $J({}^{i}x)$ in (5) has the same structure as M(x) in (6). Each block matrix $J_{q,k}({}^{i}x)$ represents the block Jacobian matrix for input frequency ω_{k} and objective frequency ω_{q} and has the same size as

 $M_{q,k}(x)$. As before, each block Jacobian matrix $J_{q,k}(^{i}x)$ is a combination of two matrices:

$$J_{a,k}(^{i}x) = J_{ID(a,k)} + J_{D(a,k)}(^{i}x).$$
 (7)

The matrix $J_{ID(q,k)}$ in (7) is x-independent, and

$$\boldsymbol{J}_{ID(q,k)} = \boldsymbol{M}_{ID(q,k)}.$$

For efficient calculations, each complex independent variable X_j of $\mathbf{x}_{\omega q}$ can be separated into real and imaginary parts, and each complex entry $Y_{j,l}$ in matrix $\mathbf{J}_{ID(q,k)}$ can also be represented by a 2×2 matrix of real entries:

$$Y_{j,l} = \begin{bmatrix} \operatorname{Re} \left\{ Y_{j,l} \right\} & -\operatorname{Im} \left\{ Y_{j,l} \right\} \\ \operatorname{Im} \left\{ Y_{j,l} \right\} & \operatorname{Re} \left\{ Y_{j,l} \right\} \end{bmatrix}.$$
(8)

Since $J_{ID(q,k)}$ is iteration number i independent, it can be calculated just once. However, the x-dependent Jacobian matrix $J_{D(q,k)}(\dot{x})$ can only be obtained using nonlinear analysis and must be reevaluated if a new Newton iteration is required.

In addition to the Jacobian matrix $J(^ix)$, solution of the circuit equations (2) using the iterative procedures (5) also requires calculation of the induced source vector $y(^ix)$ for each iteration *i*. Assuming that $y_{\omega q}(^ix)$ is the component vector of $y(^ix)$ at radian frequency ω_q , the calculation of $y_{\omega q}(^ix)$ can be performed as

$$y_{\omega q}(^{i}x) = M_{ID(q,k)}^{i}x_{\omega q} + y_{NL(\omega q)}(^{i}x), \qquad k = q \quad (9)$$

where $y_{NL(\omega q)}(^ix)$ represents the induced source vector of all the nonlinear elements at radian frequency ω_q . Both $J_{D(q,k)}(^ix)$ in (7) and $y_{NL(\omega q)}(^ix)$ in (9) can be obtained using nonlinear analysis techniques.

C. Block Newton Iteration

Equation (5) is frequently used in nonlinear circuit analysis to minimize the objective function because of its asymptotic rate of convergence. However, evaluation of the whole Jacobian can be time consuming. In a nonlinear circuit the major coupling between currents and voltages is when the components are at the same frequencies. Consequently a modified form of Newton's method [24] (block Newton) can be used.

If the number of frequencies considered in the circuit analyzed is K and the size of each $J_{q,k}(^ix)$ is $m \times m$, the dimension of the full Jacobian matrix $J(^ix)$ is $mK \times mK$. For a complete Newton's method, $J^{-1}(^ix) f(^ix)$ must be calculated. For either type of matrix factorization, PLU or QR, the arithmetic cost of the calculation on matrix $J(^ix)$ is always proportional to $(mK)^3$. However, from (7), the x-independent matrix $J_{ID(q,k)}$ is a nonzero matrix only if q = k. For most microwave nonlinear analog circuits, the values of those derivatives in the diagonal blocks $J_{D(q,q)}(^ix)$ are normally larger than those values in the off-diagonal blocks $J_{D(q,k)}(^ix)$, $q \neq k$. Therefore, by calculating only the diagonal blocks of the Jacobian matrices $J_{q,q}(^ix)$ ($q = 0,1,\cdots,K-1$), the cost of matrix calculation will be K^2 times lower than the cost for calculation of the full Jaco-

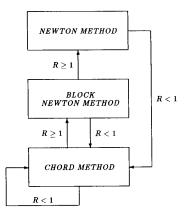


Fig. 1. The algorithm used to determine the method to be used in finding the present iterate value of x. R is the ratio between the current converged error and the previous converged error.

bian form. That is, to solve K separate iterative procedures simultaneously,

$$^{i+1}\mathbf{x}_{\omega a} = {}^{i}\mathbf{x}_{\omega a} - \mathbf{J}_{a,a}^{-1}({}^{i}\mathbf{x}) \mathbf{f}_{\omega a}({}^{i}\mathbf{x})$$
 (10)

where $q = 0, 1, \dots, K - 1$. This method uses only the diagonal blocks of the full Jacobian matrix.

Both the Newton and the block Newton method can be combined with the chord method to obtain further computational improvement. The chord method uses the previous J for the present iterative process, regardless of the method used in the previous iteration. This can save considerable Jacobian matrix calculation time and eliminates the need for matrix inversion. The convergence algorithm just described is summarized in Fig. 1.

III. GPSA ARITHMETIC OPERATOR METHOD

The GPSA arithmetic operator method (GPSA-AOM) is based on direct complex multiplications and additions. It is similar to the approach taken by Haywood and Chow [25]. However, instead of using the spectrum convolutions, a simple spectrum mapping function is used for each spectral multiplication.

If we consider the system output y(t) to be a function of two independent variables x(t) and z(t),

$$x(t) = \sum_{n=0}^{N} x_n(t) = \sum_{n=0}^{N} |X_n| \cos(\omega_n t + \phi_n)$$

and

$$z(t) = \sum_{m=0}^{M} z_m(t) = \sum_{m=0}^{M} |Z_m| \cos(\omega_m t + \theta_m)$$

the generalized power series for the output $Y_{\omega q}$ at the objective radian frequency ω_q is expressed as

$$Y_{\omega q} = \sum_{\sigma=0}^{\infty} \sum_{\rho=0}^{\infty} a_{\omega q, \sigma, \rho} \left\{ \left[\sum_{n=0}^{N} x_n (t - \tau_n) \right]^{\sigma} \cdot \left[\sum_{m=0}^{M} z_m (t - \lambda_m) \right]^{\rho} \right\}_{\omega q}$$
(11)

where ω_q is the objective frequency, σ and ρ are the orders of the power series terms, $a_{\omega q,\sigma,\rho}$ are complex coefficients in the frequency domain, and τ_n and λ_m are time delays that depend on the indexes of the input frequency components. The spectrum of x can be represented as a vector:

$$\mathbf{s}_{x} = \frac{1}{2} \begin{bmatrix} X_{-\omega K} & \cdots & X_{-\omega 1} & 2X_{0} & X_{\omega 1} & \cdots & X_{\omega K} \end{bmatrix}^{T}$$
(12)

where $X_{\omega k} = X_{-\omega k}^*$ and "*" indicates the complex conjugate. The spectrum vector for z(t) is similarly defined. For analysis purposes, we may simplify (11) to be an ordinary two-variable-dependent power series by using phase shifting of the spectra of x(t), z(t), and y(t) to eliminate time delays and complex coefficients:

$$y(t) = \sum_{\sigma=0}^{\infty} \sum_{\rho=0}^{\infty} a_{\sigma,\rho} x^{\sigma}(t) z^{\rho}(t).$$
 (13)

The output spectrum s_y for the basic operation y = xz can be found from the product of s_x and s_z^T . Let S_y be the matrix of the product of s_x and s_z^T :

$$\mathbf{S}_{v} = \mathbf{s}_{x} \mathbf{s}_{z}^{T}. \tag{14}$$

Then each element of S_y represents a component of the spectrum of the signal y, and has the value $X_{\omega i}Z_{\omega j}$ and frequency $\omega_i + \omega_j$. By combining the same frequency entries, S_y can be converted into a vector form s_y :

$$s_y = \frac{1}{2} \begin{bmatrix} Y_{-2\omega K} & \cdots & 2Y_0 & \cdots & Y_{2\omega K} \end{bmatrix}^T$$
. (15)

The contributions of higher order cross products of s_x and s_z can be obtained using the recursive application of (14) and (15).

Implementation in the Newton iteration process requires derivatives of the value in each entry of vector s_y with respect to all the different independent variables X and Z. These derivatives of each entry in s_y can be obtained by using the chain rule and complex multiplications and additions.

The calculations for system outputs presented in (14) and its derivatives are basic components of GPSA-AOM. Some techniques can also be used to modify this method to obtain increased computational efficiency.

A. Spectrum Limiting

Equation (14) shows that the spectrum vector s_y of the dependent variable y(t) for y(t) = x(t)z(t) can be created from the product of the two spectrum vectors s_x and s_z of the two independent variables x(t) and z(t). However, not all of the entries in s_y are required as objective frequencies, and some of the rest are useful for the higher order calculations only. Calculating those undesired entries will consume much computer computation time and contribute little to the final results. Therefore, if the number of objective frequencies considered in a system is large enough to describe the system nonlinearities, the frequency components of the spectrum of y can be limited to contain

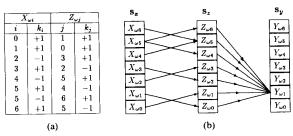


Fig. 2. An example of spectrum mapping for GPSA-AOM. (a) A mapping function used for the objective frequency ω_1 . (b) The required complex multiplications for the objective frequency ω_1 using the mapping defined in (a).

these frequencies only. Any additional frequencies which are created by the product of s_x and s_z can be eliminated from s_y .

B. Spectrum Mapping

To evaluate the nonlinear system output y(t) = x(t)z(t) with spectrum limiting on all the dependent and independent variables, some complex multiplications are discarded. These redundant multiplications will also consume much time in frequency searching operations. To avoid this redundancy, a mapping function can be used. This results in considerable computation time reduction when the frequencies under evaluation are poorly correlated.

The spectrum mapping function relates the components of the output spectrum to the spectra of two inputs where the output is the product of the two inputs. Fig. 2 is an example. Assume two sinusoidal signals of 3 GHz and 3.05 GHz are applied to a nonlinear system, and the considered frequencies include all the first- and second-order intermodulation products. For second-order intermodulation product IF $(f_1) = 50$ MHz, assuming RF $(f_2) = 3$ GHz and $LO(f_3) = 3.05$ GHz, the mapping function of y = xz can be set as shown in Fig. 2(a). In Fig. 2(a), i and j represent the frequency indexes used in each multiplication and k being "-1" indicates that the indicated component of Xor Z is to be complex conjugated. For instance, if i = 3, $k_i = +1$, j = 2, $k_j = -1$, the complex multiplication for this combination is $X_{\omega 3}Z_{\omega 2}^*$. Since all the dependent and independent variables are spectrum limited to the same length, the same spectrum mapping function can be used for the product of any two different variables if the considered objective frequency is the same. Moreover, the same function can be used in derivative calculations. Fig. 2(b) illustrates the usage of spectrum mapping to insert all the results of the required complex multiplications of $X_{\omega i}$ and $Z_{\omega i}$ yielding the entry $Y_{\omega 1}$.

C. Power Series Factoring

Factoring of the power series can be used to reduce the amount of computation. Consider a one-dimensional nonlinearity defined by

$$y = a_0 + a_1 x + a_2 x^2 + \dots + a_N x^N.$$
 (16)

This requires (N-1) spectral multiplication operations.

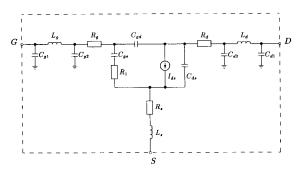


Fig. 3. Circuit used to model the MESFET which includes linear as well as nonlinear elements. Nonlinear elements include C_{gs} , C_{ds} , C_{gd} , and I_{ds} , where I_{ds} is a function of both intrinsic voltages V_{gs} and V_{ds} .

Factoring the power series, the maximum order of x in (16) can be reduced to half,

$$y = (b_0 + b_1 x + \dots + b_{N/2} x^{N/2})^2 + (c_0 + c_1 x + \dots + c_{(N/2)-1} x^{(N/2)-1})$$
(17)

if N is even, or

$$y = a_0 + x \left[\left(d_0 + d_1 x + \dots + d_{(N-1)/2} x^{(N-1)/2} \right)^2 + \left(e_0 + e_1 x + \dots + e_{(N-3)/2} x^{(N-3)/2} \right) \right]$$
(18)

if n is odd. Now N/2 (or (N+1)/2) operations are needed for (17) (or (18)). Each operation involves many floating point calculations, and the amount of computation required to factor the power series is relatively small. If N is large, factoring the power series once can result in up to 50 percent reduction in computer time.

IV. Device Characterization

The arithmetic operator method was compared to measurements using a medium-power GaAs MESFET, Avantek AT8250, and the transistor model of Fig. 3. In the equivalent circuit, C_{gs} , C_{gd} , and C_{ds} were taken to be one-dimensional nonlinear elements. The procedure used to determine the model parameters is described in [19], and yielded the linear element values of Table I. With the linear element values fixed, the model of Fig. 3 was optimized to match the measured s parameters at each bias setting, resulting in nonlinear element values as a function of bias voltage. A least-squares technique was used to fit power series to these data. The power series descriptions of the capacitances (C_{gs} , C_{gd} , and C_{ds}) are shown in Table II and their optimized values are compared to their power series representations in Figs. 4 and 5.

The nonlinear element I_{ds} is a function of both intrinsic V_{gs} and V_{ds} (i.e., the voltages across C_{gs} and C_{ds} respectively) and has a bivariate power series description as shown in Table III. Fig. 6 shows the optimized values of the transconductance G_m compared to the values calculated from the bivariate power series expansion. Fig. 7 shows the values of I_{ds} with respect to intrinsic V_{gs} and V_{ds} calculated from this power series. The valid range of this

TABLE I THE LINEAR ELEMENT VALUES USED IN THE MODEL OF THE AVANTEK AT-8250 GAAS MESFET

Element	Value
C_{g1}	0.1386 pF
L_{g}	0.69414 nH
C_{g2}	0.30707 pF
R_g	2.9 Ω
R_s	2.4 Ω
L_s	0.00323 nH
R_d	5.3 Ω
L_d	0.41143 nH
C_{d2}	0.09012 pF
C_{d1}	0.00341 pF
R_1	10 Ω
τ	6.56 pS

TABLE II
THE POWER SERIES COEFFICIENTS USED IN THE MODEL OF THE
AVANTEK AT-8250 GAAS MESFET

Power	r Series Coefficie	ents for Nonline	ear Elements	
· · · · · · · · · · · · · · · · · · ·	Element			
Order	C_{ds} (pF)	C_{gs} (pF)	C_{gd} (pF)	
0	0.286	0.62039	0.34697	
1	-0.022345	0.792475	-0.33135	
2	0.0043288	-0.02648	0.1576	
3	-0.0003038	-0.22036	-0.032687	
4			0.0024389	

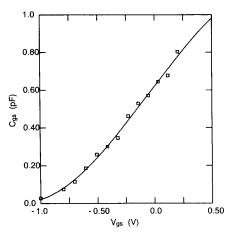


Fig. 4. Optimized values of the gate–source capacitance $C_{\rm gs}$ as a function of gate–source voltage. The points are the optimized values and the curves are the power series representations.

power series description is from $V_{gs} = -1.7$ V to $V_{gs} = 0.75$ V and $V_{ds} = 0.4$ V to $V_{ds} = 5$ V, corresponding to an input power of about 10 dBm.

V. RESULTS AND DISCUSSION

GPSA-AOM has been implemented in a C language program called FREDA2 (for FREquency Domain Analysis). This is a net-list-based program and can handle arbitrarily large nonlinear analog circuits. Results of simula-

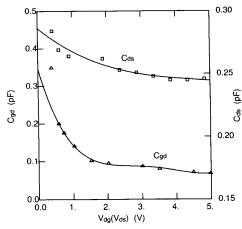


Fig. 5. Optimized values of the drain-source capacitance C_{ds} as a function of drain-source voltage and the gate-drain capacitance C_{gd} as a function of drain-gate voltage. The points are the optimized values and the curves are the power series representations.

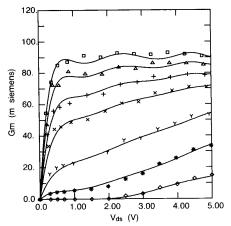


Fig. 6. The transconductance as a function of intrinsic drain-source voltage. Optimized values (points) are compared to the values calculated from the bivariate power series expression for intrinsic gate-source voltages of -0.8, -0.6, -0.4, -0.2, -0.1, 0.0 and 0.1 V.

tions using FREDA2 and the two-dimensional circuit model of Fig. 3 are compared to measurements in Figs. 8 and 9 for single-tone and two-tone excitations. The simulated results (curves) are in good agreement with the experimental results (points). Computer run time for the single-tone test is presented in Table IV for various numbers of frequencies considered. Times are given for different input powers (-10, 0, and 10 dBm) with zero initial guess, and for a sweep of the input power from -10 dBm to +10 dBm with 0.5 dB step size. (While sweeping the input power, the initial guesses of the variable values in each power step are based on the final values of the previous step.)

The primary advantage of the arithmetic operator method in frequency-domain spectral balance, compared to the earlier reported table method, is the 2-D model simulation capability. Even with this extended capability, AOM is about ten times faster than TM [19], [26]. AOM

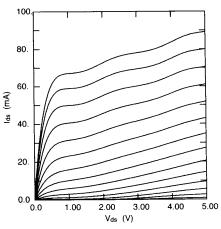


Fig. 7. The drain-source current as a function of intrinsic drain-source voltage for the Avantek AT-8250 GaAs MESFET. The values are calculated from the bivariate power series expression for intrinsic gate-source voltages of -0.8 V to 0.5 V in 0.1 V steps.

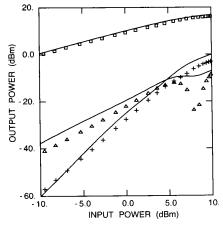


Fig. 8. The results of the single-tone test for the Avantek AT-8250 using a 3 GHz fundamental. Shown are the simulated (curves) and measured values (points) for the power output at the fundamental, the second harmonic, and the third harmonic as a function of input power.

has good convergence properties and simulation can be achieved with large input powers and zero initial guess. However, if a sweep of the input power is required, it is more efficient to use the results of the previous lower input power simulation as an initial guess of the circuit variables. We attribute the good convergence properties to the use of analytic derivatives rather than numerical derivatives. The analytic derivatives need to be calculated to double precision to obtain convergence at the higher input power levels if the system nonlinearities are modeled with high-order power series. This level of precision cannot readily be obtained by taking derivatives numerically.

As mentioned in Section II, a mix of the Newton, block Newton, and chord methods is used in the simulation to obtain an efficient simulator. This iteration scheme greatly enhances the speed of FREDA2. For almost every case in the single-tone and two-tone test, one or two block Newton methods followed by several chord methods are enough

0.0009928612

0.0002540126

0.01847188

0.6183949

-1.461959

0.1523607 - 0.3733957

AVANIER A 1-02-0 GAAS MEST ET								
Order of				Order of	V _{ds} Term			
V_{gs} Term	0	1	2	3	4	5	6	7
0	-0.00001156555	0.07625879	-0.1056094	0.07761516	-0.0313192	0.007058957	-0.0008327161	0.00004002119
1	-0.00006103041	0.3069145	-0.4733609	0.3675621	-0.1555473	0.03641407	-0.00442661	0.0002179638
2	0.002515452	0.3475555	-0.6445519	0.5434066	-0.2414711	0.05823406	-0.007202639	0.0003578266
3	0.01210829	-0.2693955	0.4357904	-0.3522143	0.1566408	-0.03871469	0.004976994	-0.0002589956
4	0.01565735	-0.8516739	1.804808	-1.620442	0.7466659	-0.184505	0.0232377	-0.001171287
5	-0.004989491	-0.2561477	0.7083704	-0.6889429	0.3238367	-0.07946468	0.009787713	-0.0004777891
6	-0.02538143	0.6769809	-1.477119	1.344364	-0.6245825	0.1552918	-0.01966353	0.0009961386

1.367254

0.3534024

-0.6390305

-0.1655718

0.1582496

0.04091728

TABLE III THE BIVARIATE POWER SERIES COEFFICIENTS USED FOR I_{ds} in the Model of the Avantek AT-8250 GaAs MESFET

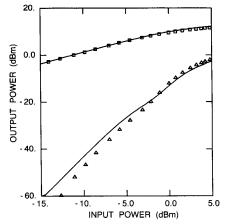


Fig. 9. The results of the two-tone test for the Avantek AT-8250 using two equal-amplitude signals input at 2.35 GHz and 2.4 GHz. Shown are the simulated (curves) and measured values (points) as a function of input power for the power output at 2.35 GHz, and the third-order intermodulation product at 2.3 GHz.

TABLE IV Computer Run Times for the Single-Tone Test

				Input power sweeps
No. of	Input Power	Input Power	Input Power	from -10 dBm to 10 dBm
Freq.	-10 dBm	0 dBm	+10 dBm	with step size 0.5 dB
3	1.6	1.9	4	30
4	2.3	2.7	6	53
5	3.1	3.9	8	83
6	4.2	5.1	11	123
7	5.2	6.6	14	173
8	6.7	8.3	17	234
9	8.2	10.3	21	300

Times are given in seconds and were measured on a VAX station 3200. The number of frequencies considered is the number of harmonics plus 1 for the

to reduce the system errors to the required limit. Using the Newton method with full Jacobian matrix evaluation instead of the block Newton method will lengthen simulation time by a factor of K^2 , where K is the number of objective frequencies.

VI. CONCLUSION

In this paper we presented a new frequency-domain spectral balance method, called generalized power series analysis using the arithmetic operator method (GPSA-AOM). Coupled with an enhanced convergence algorithm using a mix of Newton, block Newton, and chord methods, GPSA-AOM is suited to the analysis of nonlinear analog circuits. The technique is straightforward and can handle two-dimensional nonlinear elements in the frequency domain. The simulation of a MESFET amplifier with single-tone and two-tone inputs was compared to measurements to verify the method.

0.01984881

0.005108844

REFERENCES

- N. Wiener, "Response of a nonlinear device to noise," M.I.T. Radiation Lab. Rep. V-16S, Apr. 1942.
 E. Bedrosian and S. O. Rice, "The output properties of Volterra
- [2] E. Bedrosian and S. O. Rice, "The output properties of Volterra systems (nonlinear systems with memory) driven by harmonic and Gaussian inputs," *Proc. IEEE*, vol. 59, pp. 1688–1707, Dec. 1971.
- [3] J. J. Bussgang, L. Ehrman, and J. W. Graham, "Analysis of nonlinear systems with multiple inputs," *Proc. IEEE*, vol. 62, pp. 1088-1119, Aug. 1974.
- [4] S. Narayanan, "Transistor distortion analysis using Volterra series representation," *Bell Syst. Tech. J.*, vol. 46, pp. 991–1024, May-June 1967.
- [5] C. L. Law and C. S. Aitchison, "Prediction of wide-band power performance of MESFET distributed amplifiers using the Volterra series representation," *IEEE Trans. Microwave Theory Tech.*, vol. MTT-34, pp. 1308-1317, Dec. 1986.
- [6] R. A. Minasian, "Intermodulation distortion analysis of MESFET amplifiers using Volterra series representation," *IEEE Trans. Microwave Theory Tech.*, vol. MTT-28, Jan. 1980.
- [7] M. Fliess, M. Lamnabhi, and F. Lamnabhi-Lagarrigue, "An algebraic approach to nonlinear functional expansions," *IEEE Trans. Circuits Syst.*, vol. CAS-30, pp. 554–570, Aug. 1983.
- [8] M. Lamnabhi, "A new symbolic calculus for the response of nonlinear systems," Syst. and Control Lett., vol. 2, pp. 154–162, Oct. 1982
- [9] M. Lamnabhi, "Functional analysis of nonlinear circuits: A generating power series approach," *Proc. Inst. Elec. Eng.*, vol. 133, Pt. H, pp. 375–384, Oct. 1986.
- [10] C. A. A. Wass, "A table of intermodulation products," J. Inst. Elec. Eng., vol. 95, Part III, pp. 31-39, 1948.
- [11] G. M. Engel, D. A. Conner, and W. J. Steen, "Determination of intermodulation product amplitudes," in *Proc.* 1967 Nat. Electron. Conf., pp. 719-723.
- [12] R. G. Sea, "An algebraic formula for amplitudes of intermodulation products involving an arbitrary number of frequencies," *Proc. IEEE*, vol. 56, pp. 1388–1389, Aug. 1968.
- [13] R. G. Sea and A. G. Vacroux, "On the computation of intermodulation products for a power series nonlinearity," *Proc. IEEE*, pp. 337–338, Mar. 1969.
- [14] G. L. Heiter, "Characterization of nonlinearities in microwave devices and systems," *IEEE Trans. Microwave Theory Tech.*, vol. MTT-21, pp. 797–805, Dec. 1973.
- [15] R. S. Tucker and C. Rauscher, "Modelling the 3rd-order intermodulation-distortion properties of a GaAs FET," J. Electron Lett., vol. 13, pp. 508-509, Aug. 1977.
- [16] R. S. Tucker, "Third-order intermodulation distortion and gain compression in GaAs FET's," *IEEE Trans. Microwave Theory Tech.*, vol. MTT-27, pp. 400-408, May 1979.

- [17] J. A. Higgins and R. L. Kuvas, "Analysis and improvement of intermodulation distortion in GaAs power FET's," *IEEE Trans. Microwave Theory Tech.*, vol. MTT-28, pp. 9–17, Jan. 1980.
- [18] M. B. Steer and P. J. Khan, "An algebraic formula for the complex output of a system with multi-frequency excitation," *Proc. IEEE*, pp. 177-179, Jan. 1983.
- [19] G. W. Rhyne and M. B. Steer, "Generalized power series analysis of intermodulation distortion in a MESFET amplifier: Simulation and experiment," *IEEE Trans. Microwave Theory Tech.*, vol. MTT-35, pp. 1248–1255, Dec. 1987.
- [20] G. W. Rhyne, M. B. Steer, and B. D. Bates, "Frequency-domain nonlinear circuit analysis using generalized power series," *IEEE Trans. Microwave Theory Tech.*, vol. 36, pp. 379–387, Feb. 1988.
- [21] G. W. Rhyne, "Computer aided analysis of nonlinear microwave analog circuits," Ph.D. thesis, CCSP-TR-23, North Carolina State University, Raleigh, NC, 1988.
- [22] J. E. Dennis and R. B. Schnabel, Numerical Methods for Nonlinear Equations and Unconstrained Optimization. Englewood Cliffs, NJ: Prentice-Hall, 1983.
- [23] J. Vlach and K. Singhal, Computer Methods for Circuit Analysis and Design. New York: Van Nostrand Reinhold, 1983.
- [24] J. M. Ortega and W. C. Reinboldt, Iterative Solution of Nonlinear Equations in Several Variables. New York: Academic Press, 1970.
- [25] J. H. Haywood and Y. L. Chow, "Intermodulation distortion analysis using a frequency-domain harmonic balance technique," *IEEE Trans. Microwave Theory Tech.*, vol. 36, pp. 1251–1257, Aug. 1988.
- [26] C. R. Chang et al., "Simulation of nonlinear RF and microwave circuits," in RF Expo East 1988 Proc., Oct. 1988, pp. 333–342.

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